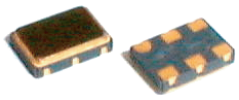

LVDS CRYSTAL OSCILLATOR IN CERAMIC LCC PACKAGE - XO75LVD1 Series

FEATURES

- RoHS Compliant (Pb-Free), LVDS Compatible Signals
- Inherent Low Power and Low EMI Emission
- Low Phase Jitter with New Generation PLL Design
- Complimentary Output, Tri-state Enable/Disable Standard, 7x5x2 mm SMD package

SPECIFICATIONS

Frequency Range	300 MHz to 700 MHz
Standard Frequency	312.500 MHz, 622.080 MHz
Input Voltage (Vcc)	B = +3.3 VDC ± 5%
Input Current	80 mA Maximum
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	100 = ±100 ppm; 50 = ±50 ppm; 25 = ±25 ppm
Temperature Range	A = 0°C to 70°C; B = -40°C to 85°C
Standard Stability	50A = ±50 ppm / 0°C to 70°C
Duty Cycle	3 = Tristate 55/45% symmetry
Output Load	100 Ohms across differential outputs (Offset 1.25V Typ)
Logic "1" / Logic "0" Level	1.43V Typ / 1.10V Typ
Rise/Fall Time (Tr/Tf)	1.0 ns Maximum at 20% to 80% Vp-p
Start-up time	5 ms Maximum
Phase Jitter (RMS, 1 Sigma)	1.6 ps Typical for fj = 12KHz to 20MHz, at 622.080 MHz
Tristate Function	Input (Pin 1) High (> 2.2V) or open: Output (Pin 4, 5) active Input (Pin 1) Low (< 0.4V): Output disabled in high impedance
Enable/Disable Time	100 ns Maximum

Creating a Part Number **XO75LVD1-622M080-B50A1**

- Product Series
- Frequency
- Supply Voltage: B = 3.3V
- Frequency Stability:
  - 100 = ±100 ppm
  - 50 = ±50 ppm
  - 25 = ±25 ppm
- Operating Temperature Range: A = 0 to 70°C; B = -40 to 85°C; X = Customized Temp Range
- Symmetry, Tristate Option (see above)

OUTLINE DRAWING